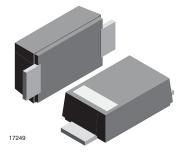


S1FLB, S1FLD, S1FLG, S1FLJ, S1FLK, S1FLM

Vishay Semiconductors

Standard Recovery Rectifier, High Voltage Surface Mount



MECHANICAL DATA

Case: DO-219AB (SMF) Polarity: band denotes cathode end Weight: approx. 15 mg Packaging codes/options: GS18/10K per 13" reel (8 mm tape), 50K/box GS08/3K per 7" reel (8 mm tape), 30K/box

Int. construction: Single

FEATURES

- For surface mounted applications
- · Low profile package
- Ideal for automated placement
- Glass passivated
- High temperature soldering: 260 °C/10 s at terminals
- Wave and reflow solderable
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PARTS TABLE					
PART	ORDERING CODE	MARKING	REMARKS		
S1FLB	S1FLB-GS18 or S1FLB-GS08	FB	Tape and reel		
S1FLD	S1FLD-GS18 or S1FLD-GS08	FD	Tape and reel		
S1FLG	S1FLG-GS18 or S1FLG-GS08	FG	Tape and reel		
S1FLJ	S1FLJ-GS18 or S1FLJ-GS08	FJ	Tape and reel		
S1FLK	S1FLK-GS18 or S1FLK-GS08	FK	Tape and reel		
S1FLM	S1FLM-GS18 or S1FLM-GS08	FM	Tape and reel		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT		
Maximum repetitive peak reverse voltage		S1FLB	V _{RRM}	100	V		
		S1FLD	V _{RRM}	200	V		
		S1FLG	V _{RRM}	400	V		
		S1FLJ	V _{RRM}	600	V		
		S1FLK	V _{RRM}	800	V		
		S1FLM	V _{RRM}	1000	V		
		S1FLB	V _{RMS}	70	V		
		S1FLD	V _{RMS}	140	V		
		S1FLG	V _{RMS}	280	V		
Maximum RMS voltage		S1FLJ	V _{RMS}	420	V		
		S1FLK	V _{RMS}	560	V		
		S1FLM	V _{RMS}	700	V		
		S1FLB	V _{DC}	100	V		
		S1FLD	V _{DC}	200	V		
Maximum DC blocking valtage		S1FLG	V _{DC}	400	V		
Maximum DC blocking voltage		S1FLJ	V _{DC}	600	V		
		S1FLK	V _{DC}	800	V		
		S1FLM	V _{DC}	1000	V		
	T _{tp} = 75 °C		I _{F(AV)}	1.5	Α		
Maximum average forward rectified current	$T_A = 65 \ ^{\circ}C \ ^{(1)}$		I _{F(AV)}	0.7	А		
Peak forward surge current 8.3 ms single half sine-wave	T _L = 25 °C		I _{FSM}	22	А		

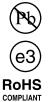
Note

⁽¹⁾ Averaged over any 20 ms periode

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THERMAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air ⁽¹⁾		R _{thJA}	180	K/W		
Operating junction and storage temperature range		T _j , T _{stg}	- 55 to + 150	°C		

Note

⁽¹⁾ Mounted on epoxy substrate with 3 mm x 3 mm Cu pads (\geq 40 µm thick)

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		S1FLB	V _F			1.1	V
	1 A ⁽¹⁾	S1FLD	V _F			1.1	V
Maximum instaneous forward voltage		S1FLG	V _F			1.1	V
		S1FLJ	V _F			1.1	V
		S1FLK	V _F			1.1	V
		S1FLM	V _F			1.1	V
	T _A = 25 °C	S1FLB	I _R			10	μA
		S1FLD	I _R			10	μA
		S1FLG	I _R			10	μA
		S1FLJ	I _R			10	μA
		S1FLK	I _R			10	μA
Maximum DC reverse current at rated		S1FLM	I _R			10	μA
DC blocking voltage	T _A = 125 °C	S1FLB	I _R			50	μA
		S1FLD	I _R			50	μA
		S1FLG	I _R			50	μA
		S1FLJ	I _R			50	μA
		S1FLK	I _R			50	μA
		S1FLM	I _R			50	μA
	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A	S1FLB	t _{rr}			1.8	μs
		S1FLD	t _{rr}			1.8	μs
		S1FLG	t _{rr}			1.8	μs
Reverse recovery time		S1FLJ	t _{rr}			1.8	μs
		S1FLK	t _{rr}			1.8	μs
		S1FLM	t _{rr}			1.8	μs
	4 V, 1 MHz	S1FLB	Cj		4		pF
		S1FLD	Cj		4		pF
Turing and a second stand as		S1FLG	Cj		4		pF
Typical capacitance		S1FLJ	Cj		4		pF
		S1FLK	Cj		4		pF
		S1FLM	Ci		4		pF

Note (1) Pulse test: 300 µs pulse width, 1 % duty cycle



TYPICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)

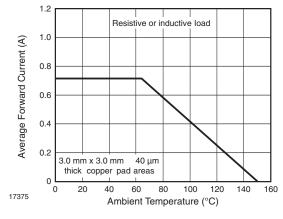


Fig. 1 - Forward Current Derating Curve

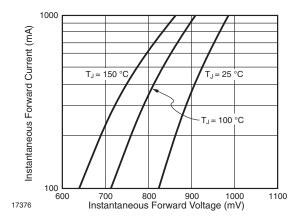


Fig. 2 - Typical Instantaneous Forward Characteristics

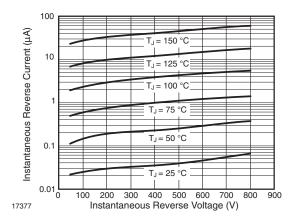


Fig. 3 - Typical Instantaneous Reverse Characteristics

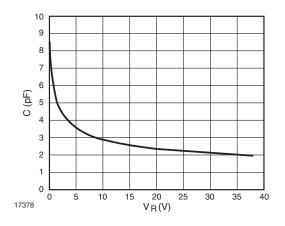
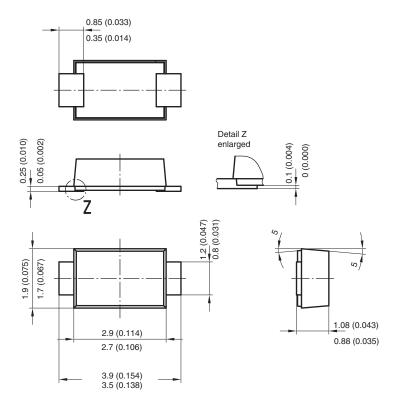


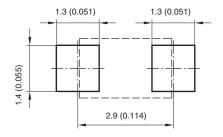
Fig. 4 - Capacitance vs. Reverse Voltage



PACKAGE DIMENSIONS in millimeters (inches): DO-219AB (SMF)



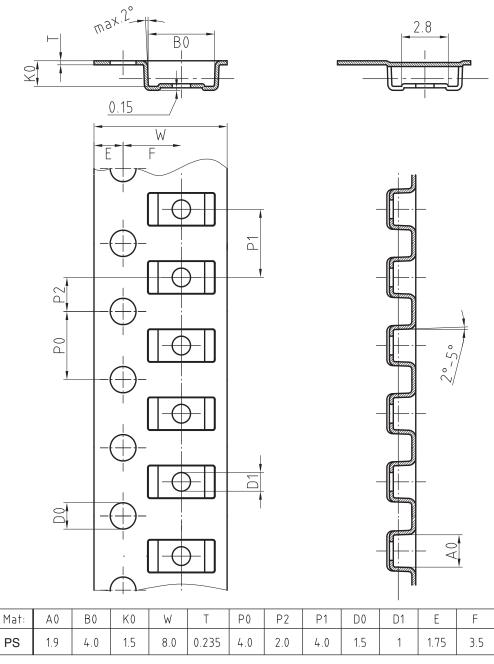
Foot print recommendation:



Created - Date: 15. February 2005 Rev. 3 - Date: 13. March 2007 Document no.:S8-V-3915.01-001 (4) 17247



BLISTERTAPE DIMENSIONS in millimeters: DO-219 AB (SMF)



Document-No.: S8-V-3717.02-001 (3) 18513

Document Number: 81820

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